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Complete if Known

09/450.381

November 29, 1999

Rajesh R. Shah

2126

Anya, Charles E.

42390P7719

SEP 17 2004

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U.S. PATENT DOCUMENTS

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Substitute for form 1449A/PTO				Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Application Number	09/450,381
				Filing Date	November 29, 1999
				First Named Inventor	Rajesh R. Shah
				Art Unit	2126
				Examiner Name	Anyia, Charles E.
Sheet	2	of	2	Attorney Docket Number	42390P7719

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.†	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T‡
CA	5	PCI Local Bus Specification, Production Version, Revision 2.1, June 1, 1995, 1992, 1993, 1995 PCI Special Interest Group.	
CA	6	Virtual Interface Architecture Specification, Draft Revision 1.0, December 4, 1997, 1997 Compaq Computer Corp., Intel Corporation, Microsoft Corporation.	
CA	7	Next Generation I/O Link Architecture Specification: Fabric Service Interface Specification, Release 1.0.0: July 20, 1999.	
CA	8	Next Generation I/O Link Architecture Specification: HCA Specification, Release 1.0.0: July 20, 1999.	
CA	9	Next Generation I/O Link Architecture Specification: Link Specification, Release 1.0.0: July 20, 1999.	
CA	10	Next Generation I/O: Principles of Operation, Release 1.0.0: July 20, 1999.	
CA	11	Next Generation I/O Link Architecture: Switch Specification, Release 1.0.0: July 20, 1999.	
CA	12	Next Generation I/O Link Architecture Specification: Channel Interface Specification, Release 1.0.0: July 20, 1999.	

Examiner Signature	CHARLES ANYIA	Date Considered	12/29/05
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